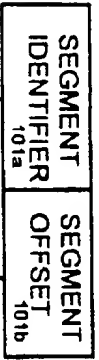


VIRTUAL ADDRESS
101
ADDRESS
TRANSLATION
UNIT
100
ORIGINAL



16

32

SEGMENT
DESCRIPTOR
REGISTER



32

OTHER
ATTRIBUTES
(183-186)



102

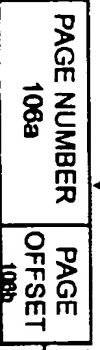
32



105

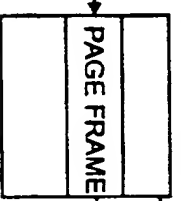
32

LINEAR
ADDRESS



20

PAGE CACHE



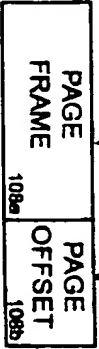
107

107a

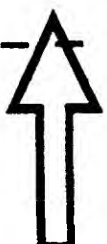
SEGMENTATION UNIT 130

20

12



PHYSICAL
ADDRESS
108



180

PAGING UNIT 150

FIGURE 1
Prior Art

08905356.080497

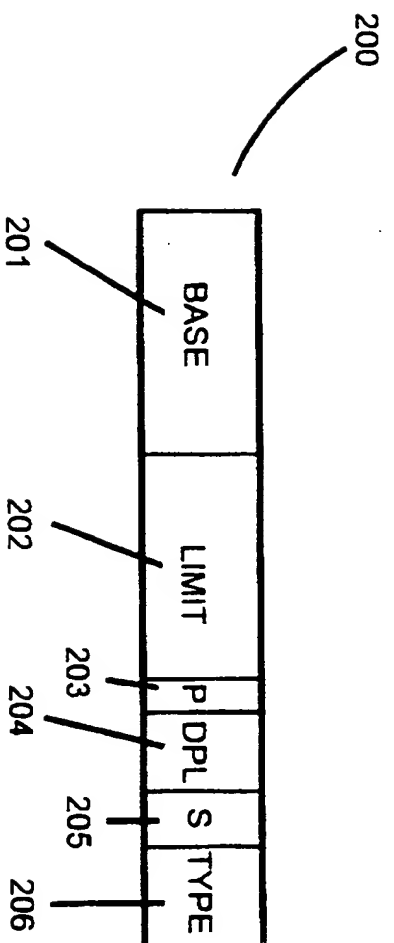


FIGURE 2A
PRIOR ART SEGMENT DESCRIPTOR REGISTER

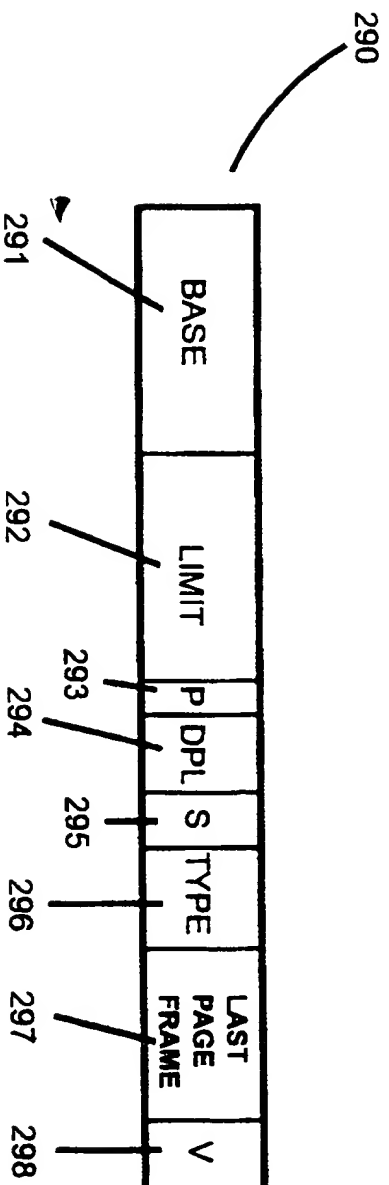


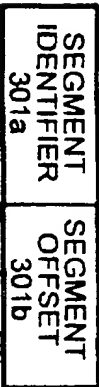
FIGURE 2B

SEGMENT DESCRIPTOR MEMORY

FIGURE 2

00005356 0000497

VIRTUAL ADDRESS



LAST PAGE FRAME 397

ORIGINAL

301

16

32

SEGMENT DESCRIPTOR MEMORY 390



VALID 398

+

305

LINEAR ADDRESS 306

32

300

OTHER ATTRIBUTES (394-396)

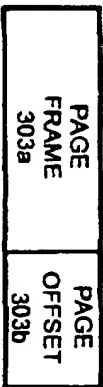
32

32

COMPARATOR

302

FAST PHYSICAL ADDRESS 303

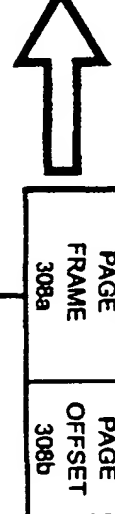


BUS INTERFACE UNIT 380

SEGMENTATION UNIT 330

380

PHYSICAL ADDRESS 308



20

12

20

CANCEL FAST PHYSICAL ADDRESS 310

≠

NOT EQUAL COMPARATOR 304

20

+

12-bit

overflow

309

low 12 bits

+

310

304

FIGURE 3A

02905356 080497

2

[illegible]

VIRUTAL ADDRESS



000000000000